



SP6018

Synchronous Rectifier Driver

DESCRIPTION

The fundamental of SP6018 synchronous rectifier (SR) driver IC is based on our U.S. patented methods that utilize the principle of “prediction” logic circuit. The IC deliberates previous cycle timing to control the SR in present cycle by “predictive” algorithm that makes adjustments to the turn-off time, in order to achieve maximum efficiency and avoid cross-conduction at the same time. Specially, SP6018 is designed for Resonance. It also maintains the MOSFET’s body diode conduction at minimum level. The SP6018 is capable to adapt in almost all existing adaptors with few adjustments considered necessary.

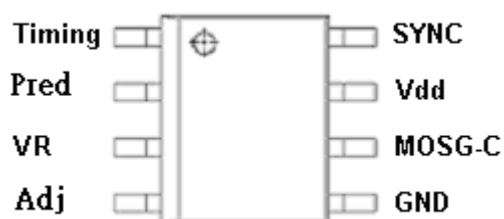
FEATURES

- Offers efficiency improvement over Schottky Diode (depends on drive configuration of the SR).
- Drives all Power MOSFET.
- Prediction gate timing control.
- Minimum MOSFET body diode conduction.
- Operating frequency up to 400 KHz.
- Synchronize to transformer secondary voltage waveform.

APPLICATIONS

- Servers & workstations
- Storage area network power supplies
- Telecommunication converters
- Embedded systems
- Industrial & commercial systems using high current processors

PIN CONFIGURATION (SOP-8)



PART MARKING

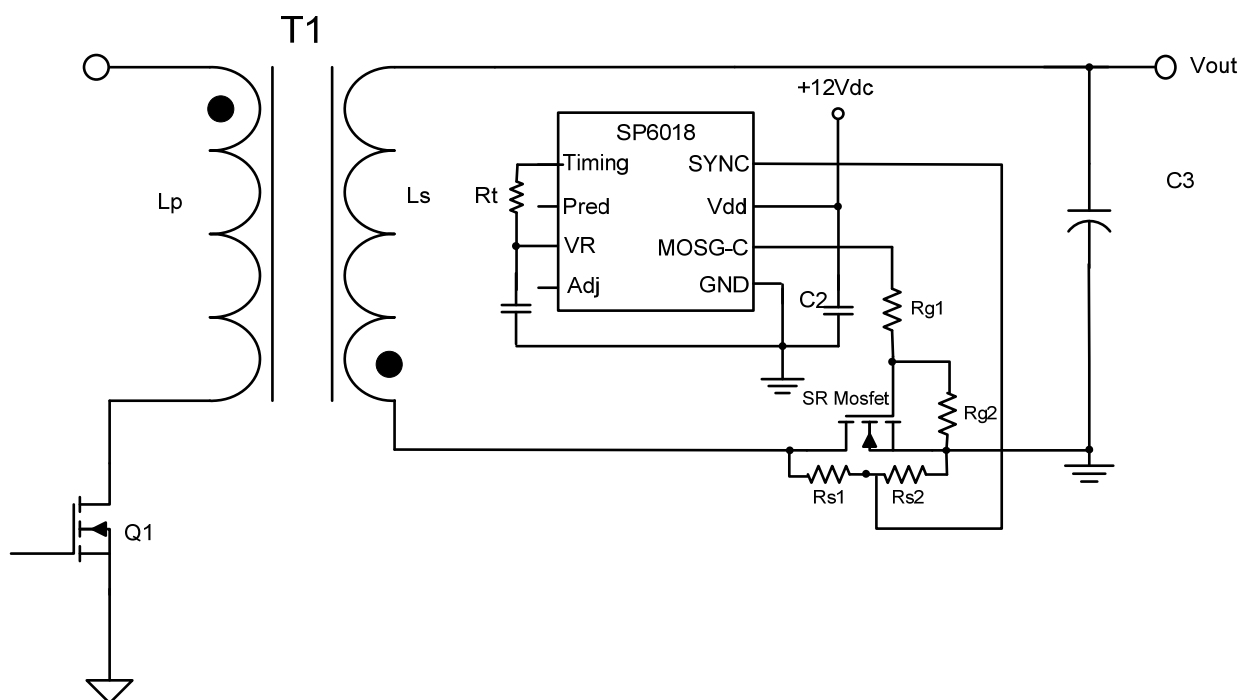




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TYPICAL APPLICATION CIRCUIT



PIN DESCRIPTION

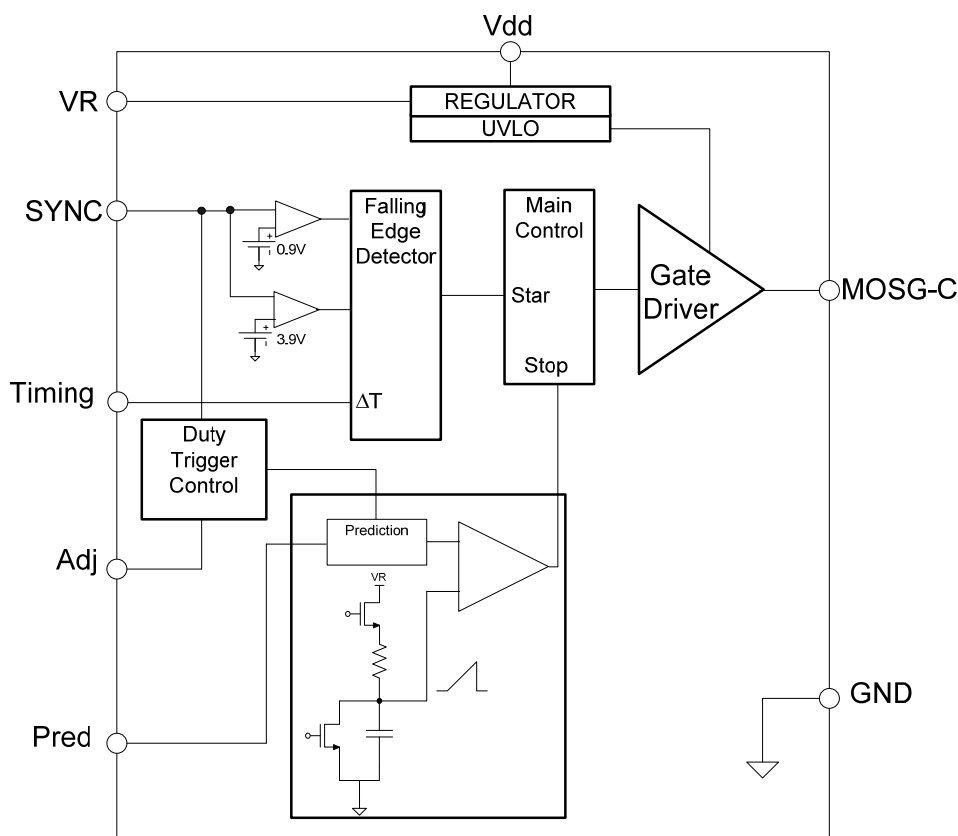
Pin	Symbol	Description
1	Timing	Discontinuous current filter timing adjustment resistor connection.
2	Pred	Capacitor to store previous cycle timing for SR MOSFET.
3	VR	Voltage Regulator.
4	Adj	Trigger point adjustment for Dynamic state.
5	GND	Ground connection.
6	MOSG-C	Catch MOSFET gate drive.
7	Vdd	DC supply voltage.
8	SYNC	Synchronized signal from the V_{DS} of SR MOSFET.



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BLOCK DIAGRAM



ORDERING INFORMATION

Part Number	Package	Part Marking
SP6018S8RGB	SOP-8	SP6018I
SP6018S8TGB	SOP-8	SP6018I

※ SP6018S8RGB : 7" Tape Reel ; Pb – Free ; Halgon – Free

※ SP6018S8TGB : Tube ; Pb – Free ; Halgon – Free

ABSOLUTE MAXIMUM RATINGS (TA=25°C, unless otherwise specified.)

The following ratings designate persistent limits beyond which damage to the device may occur.

Symbol	Parameter	Value	Unit
V _{dd}	DC Supply Voltage	16	V
I _{OUT}	Peak Source Current (Pulsed)	1	A
	Peak Sink Current (Pulsed)	1	A
P _D	Power Dissipation @ T _A =85°C (*)	0.25	W
T _J	Operating Junction Temperature Range	-40 to 125	°C
T _{STG}	Storage Temperature Range	-40 to 150	°C
T _{LEAD}	Lead Soldering Temperature for 5 sec.	260	°C



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THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance Junction – Case (*)	45	$^{\circ}\text{C/W}$

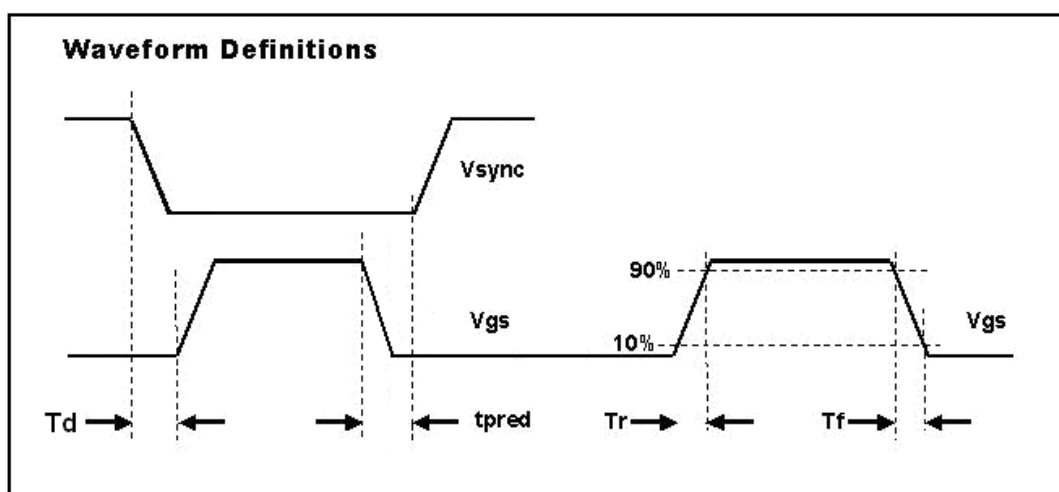
(*) The power dissipation and thermal resistance are evaluated under copper board mounted with free air conditions.

ELECTRICAL CHARACTERISTICS

($T_A=25^{\circ}\text{C}$, $V_{dd}=12\text{V}$, Freq. =300 KHz, Duty Cycle=50%, unless otherwise specified.)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
SUPPLY INPUT						
I_{DD}	Supply current	No load		4	7	mA
		$V_{SYNC}=0\text{V}$, No load		5	8	mA
V_{dd}	Supply voltage	$I_{dd_peak} < 2\text{A}$			16	V
$V_{dd\ on}$	Enable voltage		9.0	10.0	11.0	V
SYNC REFERENCE (SYNC)						
V_{shth}	SYNC high threshold			3.9		V
V_{slth}	SYNC low threshold			0.9		V
V_{sync}	SYNC clamp voltage	$I_{sync}=3\text{mA}$		5		V
I_{sync}	SYNC input current				3	mA
Voltage Regulator REFERENCE (VR)						
I_{VR}	VR Output Current				20	mA
ON TIME DUTY SETUP (PIN 6)						
$T_{on-time}$				20		us
MOSFET GATE DRIVER (MOSG-C)						
V_{oh}	Output high voltage	$I_o = -200\text{mA}$	10.5	11		V
V_{ol}	Output low voltage	$I_o = 200\text{mA}$		0.5	0.8	V
T_d	Propagation delay	No load	50	80		ns
T_{pred}		No load		120		ns
T_r	Rise time	No load		10	25	ns
T_f	Fall time	No load		10	25	ns
Dynamic Protect						
D_t	Dynamic variable	Pin 4 open		600		ns
T_{on-min}	MOSG-C on time	PWM adjusts time $> D_t$		1		us

(*) T_r & T_f are measured among 10% and 90% of starting and final voltage.





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PERFORMANCE CHARACTERISTICS ($T_A=25^{\circ}\text{C}$, unless otherwise specified.)

Figure 1 : Supply Current vs Supply Voltage

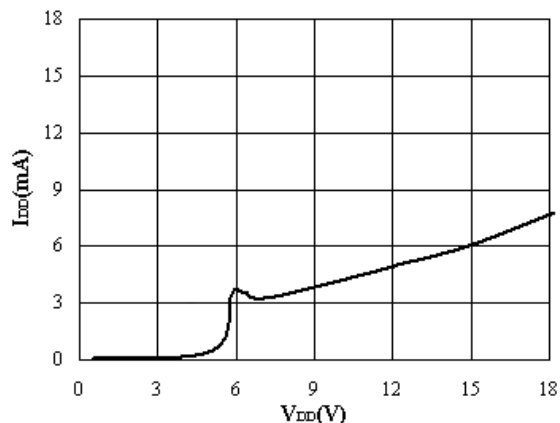


Figure 2 : Supply Current vs Freq. @ No Load

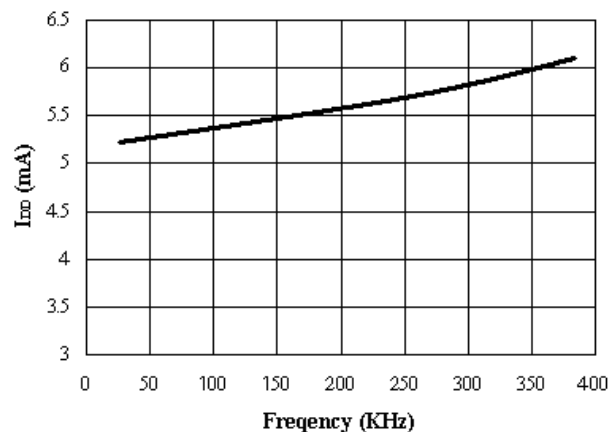


Figure 3 : T_{pred} vs C_{pred} @ Freq =100 KHz ; V_{DD} =10V

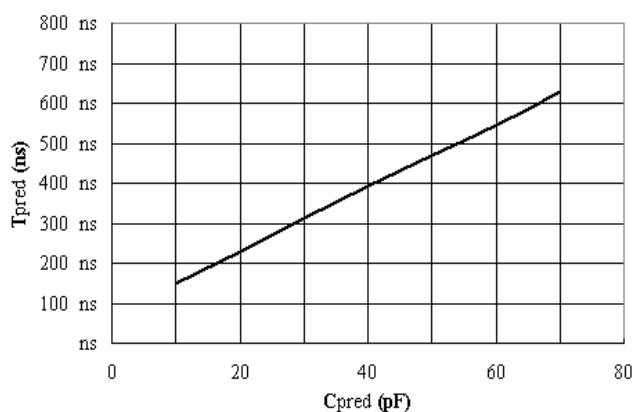


Figure 4 : Output Rise Time vs Load Capacitor

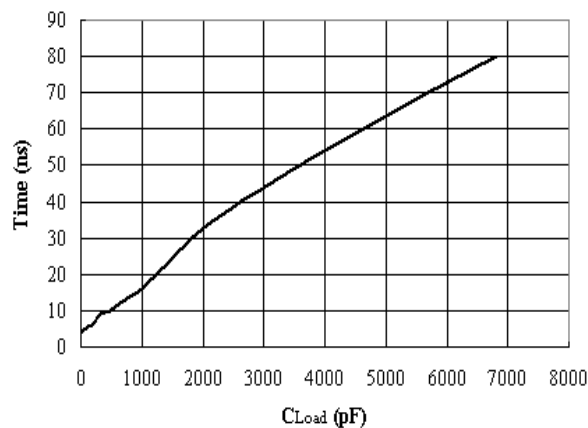


Figure 5 : Output Fall Time vs Load Capacitor

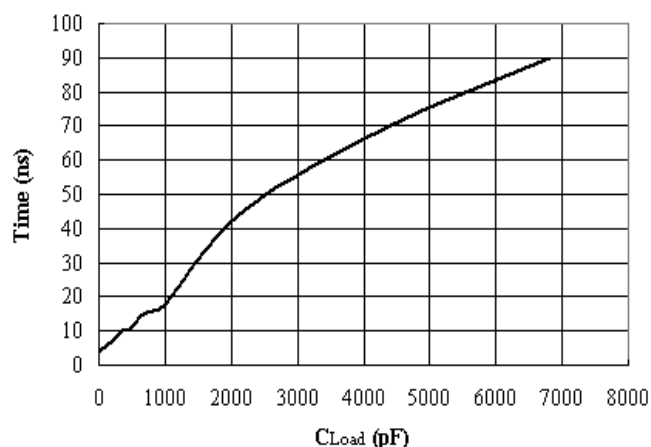
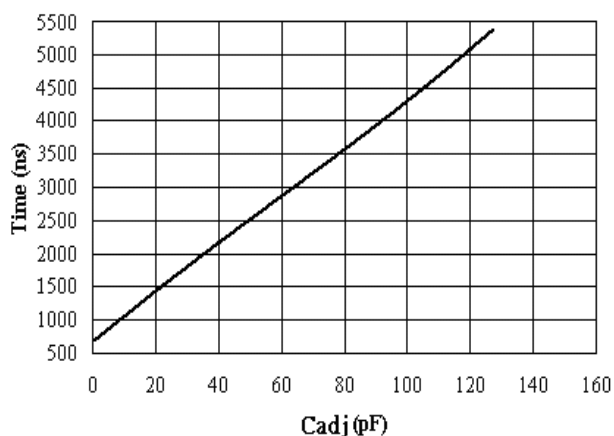


Figure 6 : Dynamic time vs Load Capacitor



*Fig. 1 : No Load ; No SYNC

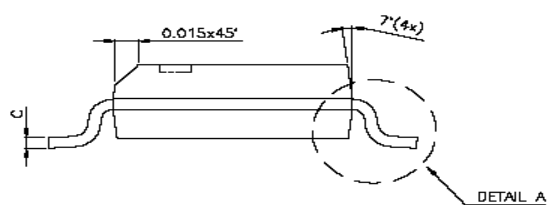
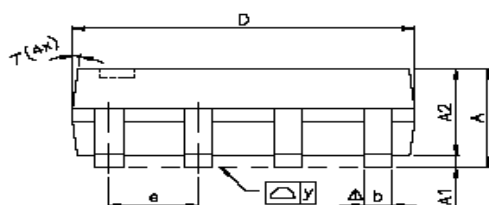
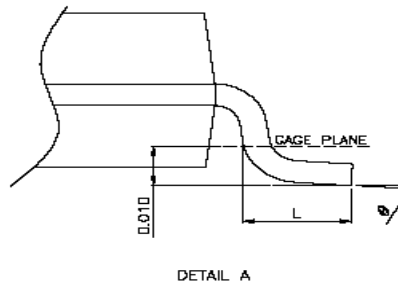
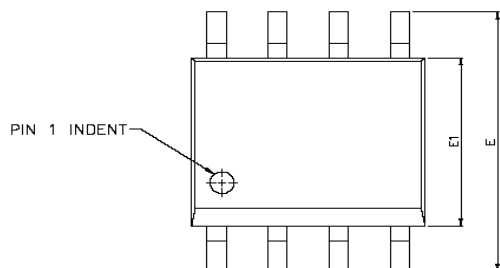
*Fig. 4~5 : Frequency = 100 kHz.



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SOP- 8 PACKAGE OUTLINE



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
Δy	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°



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